



MOTHER THERESA INSTITUTE OF ENGINEERING AND TECHNOLOGY

(UGC-AUTONOMOUS INSTITUTION)

Approved by AICTE, Affiliated to JNTUA, Anantapuramu, Accredited by NAAC & Certified by ISO 9001:2015

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Academic Year (2025-2026)

Report-On

“VLSI TECHNOLOGY”

(27-01-2026 to 31-01-2026)

The Department of ECE conducted a Five-day workshop on “VLSI TECHNOLOGY” organized for students of III Year ECE from 27-01-2026 to 31-01-2026. The program was organized by department of ECE in collaboration with YOUNGMINDS Technology Solutions Pvt.Ltd.

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
in collaboration with
YOUNGMINDS
TECHNOLOGY SOLUTIONS PVT.LTD.

Organizes a 5-Days Workshop on
“VLSI Technology”
(27-01-2026 to 31-01-2026)

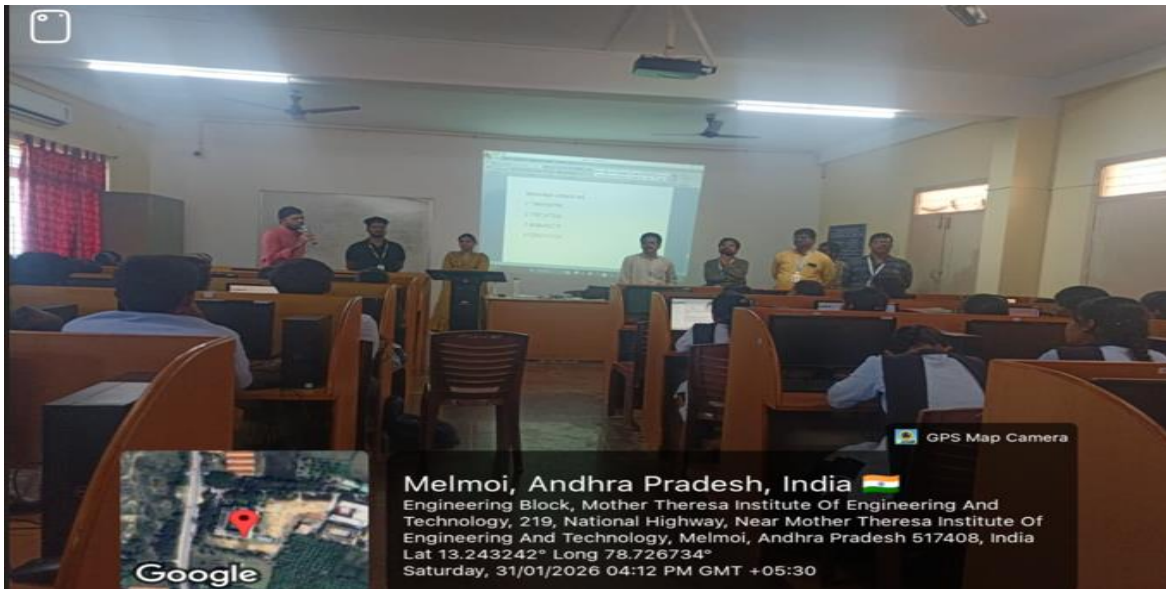
CONVENER:
Dr.L.Rama Murthy Professor
Head of the Department
CO-ORDINATOR:
Dr.S.K. Lakshminarayana Associate Professor

Dr. L. Rama Murthy, Head of the Department, is speaking at a podium during the workshop. Two other men are seated in front of a backdrop that displays the workshop details.

This program was organized by Dr. L. Rama Murthy, HoD -ECE with the co-ordination of Dr. S.K. Lakshminarayana, Associate Professor, ECE Department. Totally 133 students registered and participated in the Offline hands-on program .The Workshop targeted students and faculty in the department of ECE, focusing on Very Large Scale Integration (VLSI) technology fundamentals and applications .It is a Hands-on training in VLSI design and tools held at MTIET, the event aligns with the institutes mission to bridge academic

learning with industry skills in electronics engineering.

We provided ICT facility for conducting the program at Tech lab, Visvesvaraya Block, MTIET. Our honorable speaker of the Workshop was Mr. Vinay Angalakurthi, Founder & CEO, YoungMinds Technology Solutions Pvt. Ltd. & Takeoff Edu Group. He clearly explained the importance of VLSI Technology and VLSI concepts and design process in the present Scenario to the Students of ECE department.



The five day hands-on session provide practical exposure on latest VLSI concepts, simulation tools and design process valuable insights and guidance to participants, while the esteemed speakers shared their expertise and experiences. The event was a significant step towards fostering a culture of innovation and entrepreneurship within the institution and beyond.